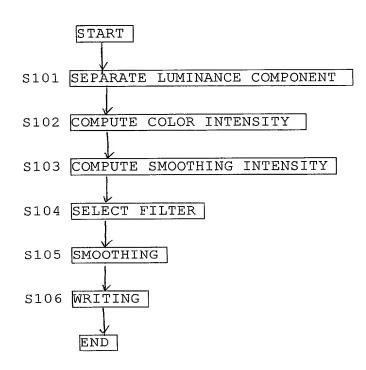
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FIG. 1

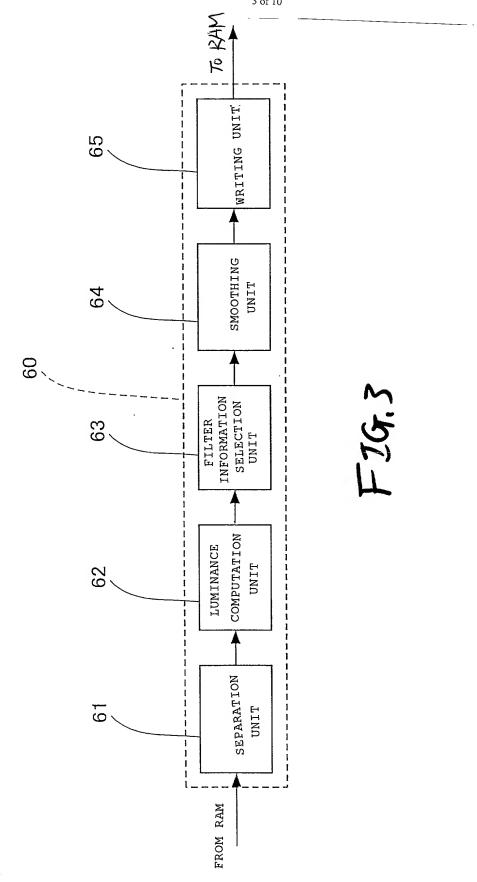


AID CONVERTER IMPUT BUTTON 10 71 72 11 12 22 23 ROM PROCESSING CIRCUIT CPU 60 31-30 --Flash 32 VRAM LCD 50 42 41 40

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FIG.Z

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22						
Mg	G	Mg	G	Mg	G	
Су	Ye	Су	Υe	Cy	Ye	
Mg	G	Mg	G	Mg	G	
Су	Ye	Су	Υe	Су	Ye	
Mg	G	Mg	G	Mg	G	

FIG4

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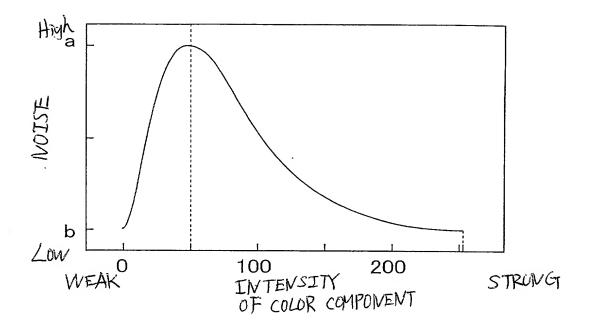


FIG.5

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		M	
P1	P2		
		Pn	
			P25

FIG.6

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 $\sigma = 1$ 

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$\sigma$ =0.5						
0	٠ 0	0	0	0		
0	3	21	3	0		
0	21	158	21	0		
0	3	21	3	0		
0	0	0	0	0	/254	

1	3	6	3	1
3	15	25	15	3
6	25	41	25	6
3	15	25	15	3
1	3	6	3	1

/253

$\sigma=2$						
6	9	10	9	6		
9	13	14	13	9		
10	14	16	14	10		
9	13	14	13	9		
6	9	10	9	6		

/260

$\sigma=3$				
8	10	10	10	8
10	11	12	11	10
10	12	13	12	10
10	11	12	11	10
8	10	10	10	8

/257

$\sigma = 4$					
9	10	10	10	9	
10	11	11	11	10	
10	11	12	11	10	
10	11	11	11	10	
9	10	10	10	9	/256
					•

$\sigma=5$						
9	10	10	10	9		
10	11	11	11	10		
10	11	11	11	10		
10	11	11	11	10		
9	10	10	10	9		
	9 10 10	10 11 10 11 10 11	9 10 10 10 11 11 10 11 11 10 11 11	9     10     10     10       10     11     11     11       10     11     11     11       10     11     11     11		

/255

$\sigma=6$					
10	10	10	10	10	
10	11	11	11	10	
10	11	11	11	10	
10	11	11	11	10	
10	10	10	10	10	/259

$\sigma=9$							
10	10	10	10	10			
10	10	10	10	10			
10	10	10	10	10			
10	10	10	10	10			
10	10	10	10	10	/250		
					•		

F16.7

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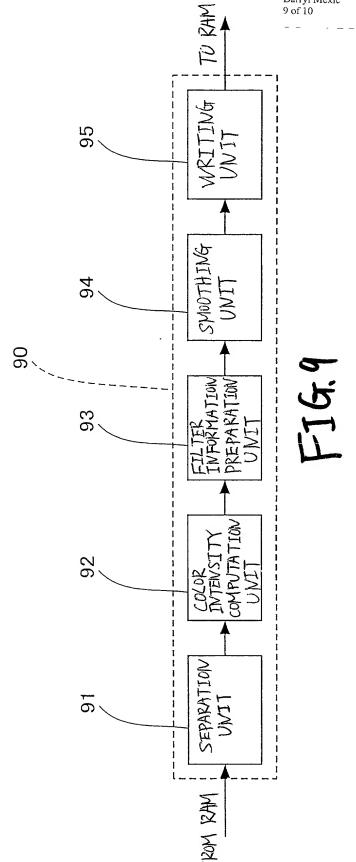
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80							
6	9	10	9	6			
9	13	14	13	9			
10	14	16	14	10			
9	13	14	13	9			
6	9	10	9	6			

FIG. 8

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FIG. 10

